

Electronic Circuit With Electrical Hole Isolator

ABSTRACT OF THE DISCLOSURE

An electronic circuit (20), comprising a semiconductor substrate (22) and a first layer (30) in a fixed physical relation to the semiconductor substrate. The electronic circuit further comprises a well (32a) formed in the first layer, wherein the well comprises a first conductivity type and has a side dimension and a bottom dimension. The electronic circuit further comprises a first enclosure (34, 26) surrounding the side dimension and the bottom dimension of the well, wherein the first enclosure comprises a second conductivity type complementary of the first conductivity type and has a side dimension and a bottom dimension. The electronic circuit further comprises a second enclosure (32b, 24) surrounding the side dimension and the bottom dimension of the first enclosure, wherein the second enclosure comprises the first conductivity type.